## IAP20 Rec'd PCT/PTO 30 JUN 2006

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

In Re PATENT APPLICATION Of:

Applicant : Mineo MIURA )

U.S. Appln. No. : To Be Assigned )

International Appln. No.: PCT/ JP04/019662 )

International Filing Date: December 21, 2004 )

For : PRODUCTION METHOD FOR SEMICONDUCTOR DEVICE )

Attorney Ref. : AI 418NP 

June 30, 2006

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(2).

Attached are copies of two Japanese patent publications, any relevance of which to the claimed invention can be gleaned from the attached English-language Abstracts, computer translations, the attached English-language international search report, and/or the discussion on page 3 of the specification. All of the publications are listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all documents listed. Consideration of the listed documents is respectfully requested.

June 30, 2006

Date

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Respectfully submitted,

RHB:tl

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT .			Docket No.:	International No.: PCT/JP041019662 585108		
			AI 418NP			
			Applicant: Mineo MIURA			
			International Filing Date:	Group Art Unit:		it:
			December 21, 2004	To Be Assigned		
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	2003-092267	03/28/03	JAPAN			Abstract and computer translation
	2001-068428	03/16/01	JAPAN			Abstract and computer translation
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